Interrupt

Refine Search

Search Results -

Terms	Documents	
L1.clm. and (PCI or "PCI Express").clm.	15	

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database US OCR Full-Text Database

Database:

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Search:

L3			Refine Search
	Recall Text	Clear	Interrupt

Clear

Search History

DATE: Monday, April 02, 2007 **Purge Queries** Printable Copy Create Case

Set Name Query Hit Count Set Name side by side result set DB=PGPB; PLUR=YES; OP=OR L1.clm. and (PCI or "PCI Express").clm. . <u>L3</u> 15 L3 <u>L2</u> L1 and (PCI or "PCI Express") 5401 L2 <u>L1</u> (port near10 connect\$4) same (mode or type or phase) 23877 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

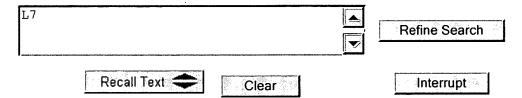
Terms	Documents
L6 and (board or card or motherboard)	695

Database:

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Search:



Search History

DATE: Monday, April 02, 2007 Purge Queries Printable Copy Create Case

Set Nam side by sid	Hit Count	Set Name result set	
DB=P	GPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR = YB	CS; $OP = OR$	
· <u>L7</u>	L6 and (board or card or motherboard)	695	<u>L7</u>
<u>L6</u>	L4 same (PCI or "PCI Express")	805	<u>L6</u>
<u>L5</u>	L4 and (PCI or "PCI Express")	7854	<u>L5</u>
<u>L4</u>	104454	<u>L4</u>	
DB=P	GPB; $PLUR=YES$; $OP=OR$		
<u>L3</u>	L1.clm. and (PCI or "PCI Express").clm.	15	<u>L3</u>
<u>L2</u>	L1 and (PCI or "PCI Express")	5401	<u>L2</u>
<u>L1</u>	(port near10 connect\$4) same (mode or type or phase)	23877	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms				
(375/376 361/679 361/683 361/783 345/520 345/531 370/254 709/253 710/100 710/33 710/300 710/301 710/302 710/72 710/306 710/313 326/37).ccls.	16879			

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L8

Refine Search

Recall Text
Clear
Interrupt

Search History

DATE: Monday, April 02, 2007 Purge Queries Printable Copy Create Case

Name Query	HII Count	<u>Na</u>
side by side	Count	res s
DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		31
<u>L8</u> 710/100,33,300- 302,72,306,313;345/520,531;361/679,683,783;709/253;326/37;375/376;370/254.cc	ols. 16879	L
<u>L7</u> L6 and (board or card or motherboard)	695	L
<u>L6</u> L4 same (PCI or "PCI Express")	805	Ţ
<u>L5</u> L4 and (PCI or "PCI Express")	7854	L
<u>L4</u> (port near10 connect\$4) same (mode or type or phase)	104454	L
DB=PGPB; $PLUR=YES$; $OP=OR$		
L1.clm. and (PCI or "PCI Express").clm.	15	L
<u>L2</u> L1 and (PCI or "PCI Express")	5401	L
<u>L1</u> (port near10 connect\$4) same (mode or type or phase)	23877	Ţ

END OF SEARCH HISTORY

<u>Set</u>

<u>S</u>

Hit

Refine Search

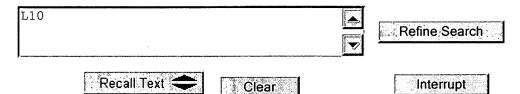
Search Results -

Terms	Documents		
L7 and L8	70		

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Search:



Search History

DATE: Monday, April 02, 2007 Purge Queries Printable Copy Create Case

<u>Set</u>	Hit	<u>S</u>
Name Query	Count	<u>Na</u>
side by side		res s
DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		3,
<u>L10</u> 17 and L8	70	<u>L</u>
<u>L9</u> 16 and L8	78	L
<u>L8</u> 710/100,33,300- 302,72,306,313;345/520,531;361/679,683,783;709/253;326/37;375/376;370/254.ccls.	16879	<u>L</u>
<u>L7</u> L6 and (board or card or motherboard)	695	L
<u>L6</u> L4 same (PCI or "PCI Express")	805	L
<u>L5</u> L4 and (PCI or "PCI Express")	7854	L
<u>L4</u> (port near10 connect\$4) same (mode or type or phase)	104454	L
DB=PGPB; $PLUR=YES$; $OP=OR$		
L3 L1.clm. and (PCI or "PCI Express").clm.	15	L
<u>L2</u> L1 and (PCI or "PCI Express")	5401	L
<u>L1</u> (port near10 connect\$4) same (mode or type or phase)	23877	<u>L</u>

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11	tranceivers same enable	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:15
L2	26509	driver same receiver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:15
L3	1857	enable same I2	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:20
L4	15017	enable\$4 near3 driver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:20
L5	12073	enable\$4 near3 receiver	ÚS-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:22
L6	3866	disable\$4 near3 driver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23
L7	2221	disable\$4 near3 receiver	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23
L8	32	14 same 15 same 16 same 17	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/04/02 09:23



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Introduction to PCI Express-a new high speed serial data bus

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speed applications in the industrial world. serial data is shifted into registers for parallel data output. The receiver also recovers the embedded clock. This new bus can be used to connect modules or boxes via twisted pair copper and is the bus of choice for high the clock in the data stream. On the transmit side parallel data is shifted out serially and on the receive side differential low voltage interconnect. The signaling rate is 2.5 Gbit per second, with 8/10 bit encoding to embed Most new PCs have a new bus called PCI Express. It is a very high speed dual-simplex, point to point serial

Index Terms

Controlled Indexing

embedded systems microcomputers peripheral interfaces system buses

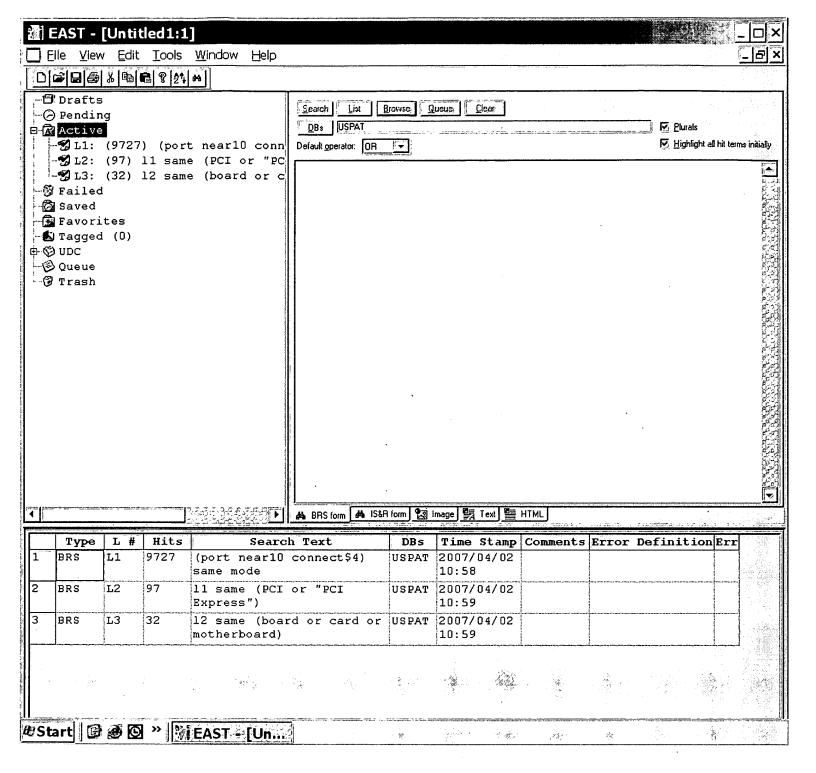
Non-controlled Indexing

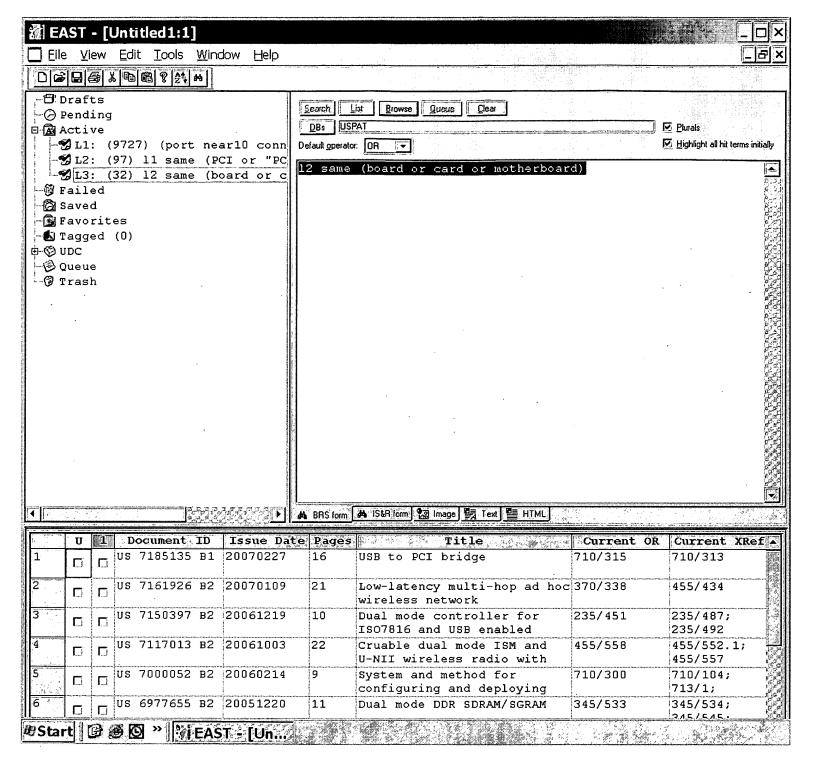
side 2.5 Gbit/s PCI Express data parallel data output registers serial data data stream embedded clock high speed serial data bus parallel point-to-point serial differential low voltage interconnect receive signaling rate transmit side twisted pair copper very high

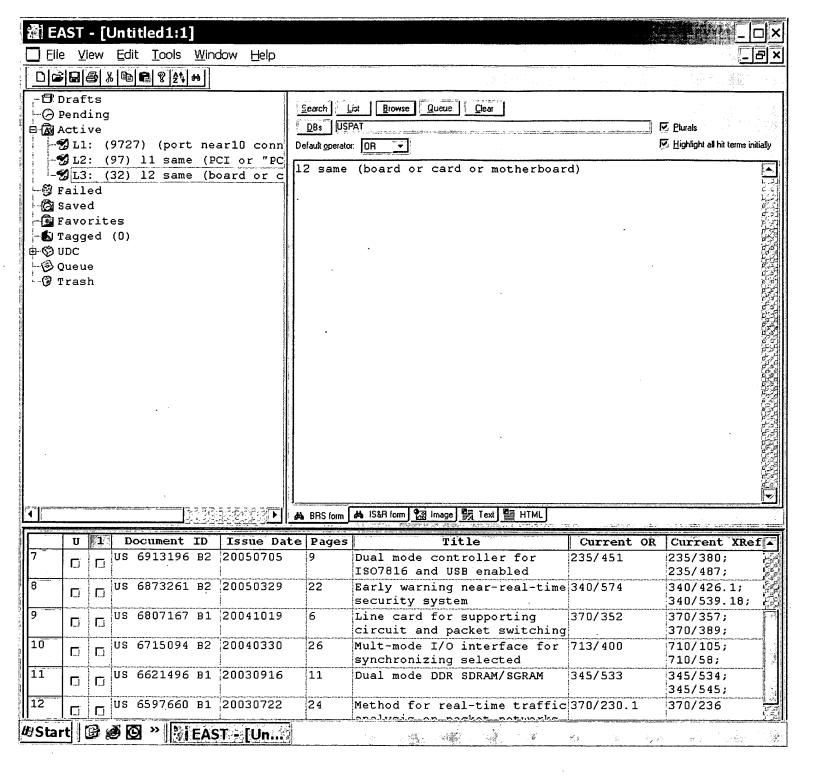
speed dual-simplex

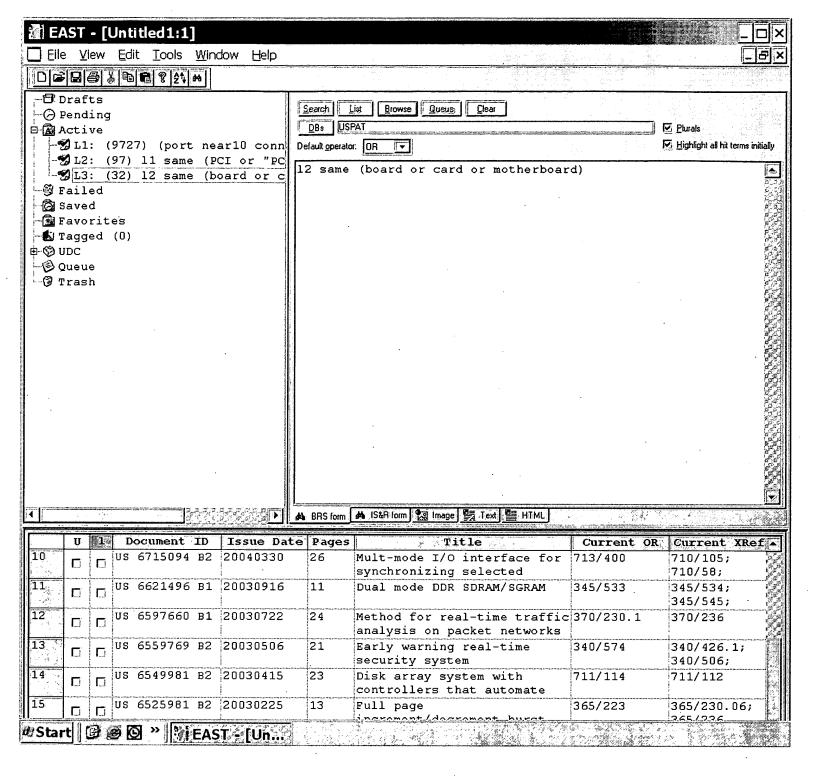
Author Keywords Not Available

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IEEE STD	IEEE Standard			olume 2, 23-29 Oct. 2005 gital Object Identifier 10.1	•		
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